



## ROBINSON INTELLECTUAL PROPERTY LAW OFFICE

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March 3, 2006

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FACSIMILE COVER SHEET

To:	Ex. Laura Schillinger	From:	Robert L. Pilaud, Reg. No. 53,470 
Facsimile Number:	571-273-1697	Telephone No.	571-272-1697
Number of Pages (including cover sheet):	8		

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 08/841,644

Filed: April 30, 1997

For: SEMICONDUCTOR DEVICE AND METHOD FOR FORMING THE SAME

Per your request, attached is a copy of the *Preliminary Amendment* filed April 30, 1997, in the subject application, including claims 23-32. Please let me know if I may be of any further assistance.

Receipt is acknowledged of: April 30, 1997

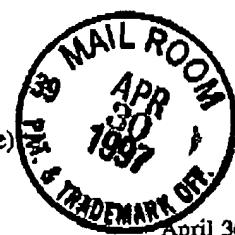
In re the Divisional Application of: Shunpei YAMAZAKI et al.  
Based on Serial No. 08/504,225 Filed: July 19, 1995  
For: SEMICONDUCTOR DEVICE AND METHOD FOR FORMING THE SAME

Rule 60 Divisional Application (duplicate) including:  
specification and claims as originally filed  
Declaration or Oath as originally filed  
18 sheets of formal drawings (Figs. 1-14)  
Preliminary Amendment  
Check # 54246 in the amount of \$770 (filing fee)  
Copy of U.S. Patent No. 5,561,075

Docket: 0756-1603  
GJF/EJR/rmf

or

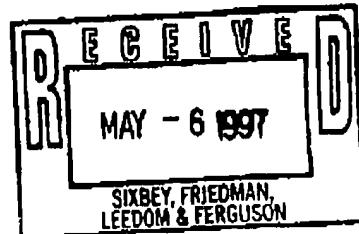
PLEASE STAMP WITH SERIAL NO. AND RETURN



April 30, 1997

HAND DELIVERY

*Name*  
5-6-97



- 1 -

Docket: 0756-1603

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of ) # 08/841 644  
Shunpei YAMAZAKI et al. )  
Serial No. To be Assigned )  
Filed: )  
For: SEMICONDUCTOR DEVICE AND )  
METHOD FOR FORMING THE SAME )

PRELIMINARY AMENDMENT UNDER 37 CFR §1.607 and  
NOTIFICATION OF OTHER AMENDMENTS UNDER 37 §1.607

IN RELATED PATENT APPLICATIONS

Honorable Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Please preliminarily amend the subject application in accordance with 37 CFR §1.607.

In the claims:

Please cancel claim 1 and add new claims 23-32 as follows:

--23. A method of manufacturing an active matrix panel in which data signals are supplied to liquid crystal layers through a plurality of thin film transistors arranged in a matrix of pixels, gate lines and data lines being coupled to each thin film transistor, said method comprising the steps of: